

SE100

~ eDP 5.4G 4K2K/120Hz) Signal Generator ~



Equipment specification

Item	Detail
Operating temperature range	0 - 40 °C
Storage temperature range	0 - 60 °C
Operating humidity range	20 - 80 % RH (Non-condensing)
Input supply voltage Power frequency	AC85 - 220V 50 / 60Hz
Body Dimensions	242(W) × 242(D) × 128(H) (does not include protrusions)
Weight	about 4.0 kg
Power consumption	Max. 375W
Dot Clock Frequency	20MHz - 350MHz
Output Signals	eDP 4 lane (1.62 / 2.7 / 5.4 Gbps)
Output Voltage	VLCD 2.0V - 13.0V (max. 5A variable) VID 3.3V or 5V (max. 2A) AUX 12.0V (max. 2A)
Resolution	Max. 4096 × 4096

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The image displays two windows of the SLB3 Editor software. The top window shows the main configuration interface for a signal generator. The left pane contains a list of configuration commands such as #INITIAL, #OVER(100), #UNIT("SE100"), #SIZE.SET(3840,2160), #FREQ.SET(34850000), #COLOR.SET(8), #BSYNC.SET.OUTTYPE("OCTAL"), #BSYNC.SET.DEN("COM","HI"), #BSYNC.SET.H(240,4,10,262,"LOW"), #BSYNC.SET.V(2160,3,20,2200,"LOW"), #REDP.SET.LANE(4,"2,7B"), #POW.SET.WV("VLCD",12000), #SIZE.SET(3840,2160), #FREQ.SET(34290000) # 548.64MHz /0, #BSYNC.SET.OUTTYPE("OCTAL"), #BSYNC.SET.H(480,10,10,508,"HI"), #BSYNC.SET.V(2160,30,30,2250,"LOW"), #MEM.W(16,0x830,0xE07D) # Octal LR Devide, and BitData configuration. The right pane shows the configuration for the #BSYNC.SET.DEN function, with arguments ["COM"] and ["HI"], and notes about waveform enablement. Waveform displays for HSYNC, VSYNC, and ENABLE are shown at the bottom of this pane.

The bottom window shows a pattern configuration interface. The left pane lists commands like #P19_UP, #P20_UP, #P21_UP, #P22_UP, #P23_UP, #P24_UP, #P25_UP, and #P26_UP, each followed by a #PAT.W.PXPAT command with various parameters. The right pane shows the configuration for the #PAT.W.PXPAT function, including pattern name, checker size (320x180), view settings (Color, Scale, Gradation, RGB Check, Color Bar, Position, Bitdata, BMP File), and a preview of a checkerboard pattern.

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